

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings of claims in the application. Applicant has submitted a new complete claim set showing marked up claims with insertions indicated by underlining and deletions indicated by strikeouts and/or double bracketing.

**Listing of Claims:**

1. (Currently Amended) A computer implemented method for executing a computer system performance analysis model, the method comprising the steps of:

first providing a workload specification comprising a set of resource use requests including at least one event;

second providing at least one hardware model, independently defined with regard to the workload specification, comprising hardware performance information associated with a resource;

third providing a hardware configuration which defines the resource and including a reference to the resource defined by the hardware model, wherein providing a hardware configuration comprises creating the hardware configuration from an XML script independently designated with respect to the workload specification; and

applying the hardware configuration to the workload specification to render performance data, wherein the applying step comprises referencing the hardware model to render performance information of the resource corresponding to the at least one event derived from the set of resource use requests.

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2. (Canceled)
3. (Currently Amended) The method of claim [[2]] 1 further comprising the step of:  
  
providing an extensible schema defining a set of syntactic rules under which the script is formulated.
4. (Original) The method of claim 3 further comprising extending the set of syntactic rules according to definitions specified for the hardware model.
5. (Canceled).
6. (Previously Presented) The method of claim 1 wherein the performance information of the resource comprises a modeled delay associated with an identified event.
7. (Original) The method of claim 6 wherein the identified event comprises a communication event.

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8. (Currently Amended) The method of claim 1 further comprising rendering an output trace in a ~~component~~hardware-specific format, the rendering step comprising:

defining a standard output trace format;

extending the standard output trace format to support ~~component~~hardware-specific traces associated with the hardware model; and

integrating standard output traces with hardware-specific traces to render an output trace in a hardware-specific format.

9. (Original) The method of claim 8 wherein output trace formats are specified by XML schemas.

10. (Original) The method of claim 1 further comprising the steps of:  
providing a set of user-specifiable instructions for controlling evaluation of a sub-set of a list of events associated with a workload specification.

11. (Currently Amended) A performance technology infrastructure facilitating integrating independently designated workload and hardware descriptions in a performance analysis, the performance technology infrastructure comprising:

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at least one memory having stored therein:

a workload specification interface including a workload request output and a hardware system configuration output;

a hardware model interface including a resource request input based on at least a portion of a workload request, a hardware performance parameter input, and a performance data output;

a component configuration database associating the hardware system configuration with a hardware performance parameter, wherein the hardware system configuration of the component configuration database is specified in a form of a script comprising an XML script, and wherein the script is independently designated with respect to the workload specification; and

an evaluation engine comprising an augmentable program structure including:

a set of slots for receiving a workload specification including the workload request ~~input~~ output via the workload specification interface, and ~~[[a]]~~ the hardware performance parameter from the component configuration database, wherein the performance data corresponding to devices specifiable within the hardware system configuration is retrieved from at least one hardware model via the hardware model interface; and

at least one processor, coupled to the at least one memory, configured to execute the evaluation engine to perform the performance analysis.

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12. (Canceled)

13. (Currently Amended) The performance technology infrastructure of claim ~~[[12]]~~ 11 further comprising an extensible configuration schema defining a set of syntactic rules under which the script is formulated.

14. (Original) The performance technology infrastructure of claim 13 further comprising a program architecture facilitating extending the set of syntactic rules according to definitions specified for the hardware model.

15. (Canceled).

16. (Previously Presented) The performance technology infrastructure of claim 11 wherein the performance data comprises a modeled delay associated with an identified event of the resource request.

17. (Original) The performance technology infrastructure of claim 16 wherein the identified event comprises a communication event.

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18. (Currently Amended) The performance technology infrastructure of claim 11 further comprising an output trace generator for rendering an output trace in a ~~component hardware~~-specific format, the generator comprising routines for facilitating performing the steps of:

defining a standard output trace format;

extending the standard output trace format to support ~~component hardware~~-specific traces associated with the hardware model; and

integrating standard output traces with hardware-specific traces to render an output trace in a hardware-specific format.

19. (Original) The performance technology infrastructure of claim 18 wherein output trace formats are specified by XML schemas.

20. (Currently Amended) The performance technology infrastructure of claim 11 further comprising:

an evaluation control interface for receiving a set of user-specifiable instructions for controlling evaluation of a sub-set of a list of events associated with a workload specification.

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21. (Currently Amended) A computer-readable storage medium having computer executable instructions for executing a computer system performance analysis model, the steps including:

first providing a workload specification comprising a set of resource use requests including at least one event;

second providing at least one hardware model, independently defined with regard to the workload specification, comprising hardware performance information associated with a resource;

third providing a hardware configuration which defines the resource and including a reference to the resource defined by the hardware model, wherein providing a hardware configuration comprises creating the hardware configuration from an XML script independently designated with respect to the workload specification; and

applying the hardware configuration to the workload specification to render performance data, wherein the applying step comprises referencing the hardware model to render performance information of the resource corresponding to the at least one event derived from the set of resource use requests.

22. (Canceled)

23. (Currently Amended) The computer-readable medium of claim [[22]] 21 further comprising computer executable instructions for performing the step of:

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providing an extensible schema defining a set of syntactic rules under which the script is formulated.

24. (Original) The computer-readable medium of claim 23 further comprising computer executable instructions for performing the step of:

extending the set of syntactic rules according to definitions specified for the hardware model.

25. (Canceled)

26. (Previously Presented) The computer-readable medium of claim 21 wherein the performance information of the resource comprises a modeled delay associated with an identified event.

27. (Original) The computer-readable medium of claim 26 wherein the identified event comprises a communication event.

28. (Currently Amended) The computer-readable medium of claim 21 further comprising computer executable instructions for rendering an output trace in a ~~component~~hardware-specific format, the rendering step comprising:

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defining a standard output trace format;

extending the standard output trace format to support ~~component~~ hardware-specific traces associated with the hardware model; and

integrating standard output traces with hardware-specific traces to render an output trace in a hardware specific format.

29. (Original) The computer-readable medium of claim 28 wherein output trace formats are specified by XML schemas.

30. (Original) The computer-readable medium of claim 21 further comprising computer executable instructions for supporting an evaluation control interface for receiving a set of user-specifiable instructions for controlling evaluation of a sub-set of a list of events associated with a workload specification.

31. (Currently Amended) A performance technology infrastructure facilitating integrating independently designated workload and hardware descriptions in a performance analysis, the performance technology infrastructure comprising:

at least one memory having stored therein:

a workload specification interface including a workload request input output and a hardware system configuration output;

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a hardware model interface including a resource request input, a hardware performance benchmark input, and a performance data output;

a component configuration database associating the hardware system configuration with the hardware performance benchmark, wherein the hardware system configuration of the component configuration database is specified in a form of a script comprising an XML script, and wherein the script is independently designated with respect to the workload specification; and

an evaluation engine comprising an augmentable program structure including:

a set of slots for receiving a workload specification including the workload request ~~input~~ output via the workload specification interface, and ~~[[a]]~~ the configuration hardware performance benchmark from the component configuration database, wherein the performance data corresponding to devices specifiable within the hardware system configuration is retrieved from at least one hardware model via the hardware model interface; and

at least one processor, coupled to the at least one memory, configured to execute the evaluation engine to perform the performance analysis.

32. (Previously Presented) The method of claim 1, wherein the computer system is a networked computer environment.

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**33. (Previously Presented) The method of claim 1, wherein the at least one event identifies a task of a modeled application and a hardware resource associated with performing the identified task.**

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